Separate Compilation for Synchronous Programs

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Abstract—Esterel and other imperative synchronous languages offer a rich set of statements, which can be used to conveniently describe complex control behaviors in a concise, yet precise way. In particular, the ability to arbitrarily nest all kinds of statements including loops, local declarations, sequential and parallel control, as well as several kinds of preemption statements leads to a powerful programming language. However, this orthogonal control, as well as several kinds of preemption statements leads to difficult problems for the modular or separate compilation, which has to deal with special problems like the instantaneous reincarnation of locally declared variables.

This paper presents a compilation procedure allowing to separately compile modules of a synchronous language. Our approach is based on two new achievements: First, we derive the information that is required for a linker to combine already compiled modules. This information is stored in a file written in an intermediate format, which is the target of our compilation procedure and the source of the linker. Second, we describe a compilation procedure for a typical imperative synchronous language to generate this intermediate format. We have implemented the approach in the upcoming version 2.0 of our Averest system\(^1\).

I. INTRODUCTION

Synchronous languages [2] like Esterel [5] and its variants [20], [27] offer many important features to fulfill the requirements imposed by embedded systems: First, it is possible to generate efficient software and hardware from the same synchronous program [4], which allows a fast simulation of the application-specific hardware as well as late changes of the hardware-software partitioning of a system. Second, it is possible to determine tight bounds on the reaction time by a simplified worst-case execution time analysis (since loops do not appear in reaction steps). Third, the formal semantics of these languages allows the application of formal methods to prove (1) the correctness of the compilation and (2) the correctness of a particular program with respect to given formal specifications [24], [25], [26], [30].

However, the compilation of synchronous languages is much more difficult than the compilation of traditional sequential languages: The concurrency available in synchronous programs has to be translated such that the resulting code can be executed without the help of an operating system for managing different processes or threads. To this end, special problems like causality and schizophrenia problems have to be solved by the compilers [6], [29], [30].

It is therefore reasonable and natural to split the overall compilation process into several phases, such that the first steps are independent of the final target code. For obvious reasons, it is desirable to store the intermediate results so that they can be reused for further compilation: First, expensive optimizations may have already been performed on the intermediate results. In particular, complex statements like the interaction of concurrent threads with abortion and suspension statements as well as instantaneous broadcast communication can already be reduced to simpler statements in a first compilation phase. Second, the intermediate code can be distributed in libraries without revealing its proprietary source (which leads to the ideas of IP-blocks used in hardware design).

Before we can present our contribution to modular compilation of synchronous programs, we need to make our terminology more precise: We refer to compilation as the translation of source code into target-independent intermediate code, while synthesis means the translation from the intermediate format to the final target code. Hence, modular compilation means that we are able to transform a set of source code files into a set of modules in the intermediate code, which can be subsequently linked before the final synthesis takes place. It may also be possible that the intermediate code modules are individually synthesized and then linked, which is modular synthesis. Thus, modular compilation is a necessary precondition for modular synthesis — but not sufficient: further preconditions and restrictions are given by the specific target code. In this paper, we only consider modular compilation.

The term module is also a very fuzzy term which we have to make more precise. In our case, modules are provided by module definitions of the source language, which are compiled to corresponding modules of the intermediate language. Modules can be used by ‘calling’ (or better instantiating) them at an arbitrary place in the program. Restrictions of the use of already defined modules lead to different forms of modularity:

- The first view, which is used by most hardware description languages, considers modules as independent components that communicate over their interfaces. Most of these languages explicitly distinguish between behavior and structure. Modules are defined by the behavioral part of the language, and they are subsequently assembled by the structural part. Although there might be a structural hierarchy (which does not change over runtime), modules of systems created by this approach all run in parallel and thus, in the same context. As there are no context statements that could preempt or restart the behavior, this kind of symmetric linking is very simple. VHDL, SDL.

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or even threads in all classical imperative programming languages follow this simple, but limited, approach.

- The second approach, which is followed by imperative synchronous languages, assumes that module instantiations are orthogonal to all other statements of the language. Thus, module instantiations can be used everywhere in the synchronous program. This gives the developer a higher level of abstraction, since sophisticated control flow as sequential/parallel execution or preemption is directly modeled in the program. A basic precondition for this approach is that linking is asymmetric since if one module instantiates another module, then the calling module defines the context of the called one. This is the view classic software languages usually have: A module is ‘called’ from another one.3

For the latter case, two degrees of modularity can be distinguished, which we call incremental and separate compilation:

- Incremental compilation requires that the compiled code for the inner module is available when the outer one is compiled. Then, the compiler is able to simply include the already compiled code to the remaining part of the system. All compilation procedures like [6], [30], [33] that are defined by a single recursive traversal over the syntax tree of the program can be easily generalized to implement incremental compilation by (1) simply storing all the compilation results of a compiled module in a file and (2) importing these results whenever a module instantiation of this module is compiled.

- In contrast, separate compilation is much more complex: It allows one to compile a module without having any knowledge about the called modules except for their interfaces, since inclusion of called modules is deferred to a later linking step. Hence, even modules can be compiled that call modules that are not yet implemented. This allows developers to compile all modules of the system in an arbitrary order and to link them at the end of the development process. Furthermore, separate compilation allows one to change a called module without the need to recompile the modules calling it (only new linking is required). Clearly, this is a very important feature to efficiently create and maintain systems consisting of many modules.

This paper presents the first algorithm that supports fully separate compilation for a typical imperative synchronous language like Esterel: Modules of the source language are translated to individual modules in an intermediate format, which does not require any information about the called modules except for their names and the types of their inputs and outputs. Our contribution is based on two new developments: First, we define a new intermediate code format called AIF (Averest Intermediate Format), which is a target-independent representation of the system in terms of simple guarded actions. Second, we develop a compilation procedure to generate this AIF code. Both developments are available in the upcoming version 2.0 of our Averest system.

A particular advantage of our separate compilation is that it also handles the instantaneous reincarnation of local variable declarations in a modular way: This means that even though module calls inside loops may generate additional reincarnations of local variables, the compiler does not have to take care about these reincarnations in an explicit way. Instead, the required reincarnations are implicitly made by generating copies of module calls in the different surfaces of the nested loops. This solution requires a sophisticated generation of names and related data structures in the intermediate code.

Our contribution is therefore quite different to previous work in this area, especially different to [37] which has a similar title. [37] ignores schizophrenia problems and the asymmetric relationship between called modules and focuses on causality problems instead4. If causality analysis is integrated to the modular compilation, an explicit causality interface like the one presented in [38] could be provided. We do not make use of such interfaces, since this would restrict our programs to acyclic dependencies.

This paper is structured as follows: In Section II, we give a brief overview over the synchronous language Quartz considered in this paper and the problems that have to be solved by compiling these programs. Section III presents our intermediate format, which must be provided by all components to be integrated by the linker (that is used for both hardware and software synthesis). Subsequently, Section IV explains how modules are compiled to intermediate code. Thereby, we focus on the problems that must be solved with regard to modular compilation. In Section V, we illustrate our approach by a small example before we draw some conclusions in Section VI.

II. SYNCHRONOUS LANGUAGES

A. Syntax and Semantics

Quartz [27] is an imperative synchronous language derived from the Esterel language [8], [7]. The common paradigm of all synchronous languages is perfect synchrony [17], [2] which means that the execution of a program is divided into micro and macro steps. The execution of micro steps is done in zero time, while the execution of a macro step requires one logical unit of time. Variables are constant during the execution of micro steps and synchronously change at macro steps. As from the programmer’s point of view, all macro steps take the same amount of logical time, concurrent threads run in lockstep and automatically synchronize at macro steps.

The introduction of a logical time scale is not only a very convenient programming model, it is also the key to generate deterministic single-threaded code from multi-threaded synchronous programs. Thus, synchronous programs can be directly executed on simple micro-controllers without using complex operating systems. Another advantage is the straightforward translation of synchronous programs to hardware circuits [3], [23], [27]. Furthermore, the concise formal

3Obviously, the second approach comprises the first one: If two modules are run in parallel, the description of their connection can be seen as a separate module consisting of parallel module calls.

4From our point of view, causality analysis is not part of the compilation, but part of the linking, since causality checking can only be done in the complete system and not yet in its individual components.
The Quartz core consists of the following statements, provided that an excellent introduction to synchronous programming. The refer instead to [27] and to the Esterel primer [7], which is statements as simple syntactic sugar. Due to lack of space, the language core, which is powerful enough to define most other application-specific hardware and software.

There are two kinds of assignments: Both immediately evaluate the right-hand side expression \( \tau \) in the current environment/macro step. Immediate assignments \( x = \tau \) instantaneously transfer the obtained value of \( \tau \) to the left-hand side \( x \), whereas delayed ones \( \text{next}(x) = \tau \) transfer this value in the following macro step. Hence, the variable environment of a macro step that executes \( x = \tau \) must already satisfy the equation \( x = \tau \) (which may lead to cyclic dependencies considered by causality analysis).

There is essentially only one basic statement that defines a control flow location, namely the pause statement. These statements are endowed with unique Boolean valued control flow location variables \( \ell \), which are true iff the control flow is currently at the statement \( \ell : \text{pause} \). Since all other statements are executed in zero time, the control flow can only rest at these positions in the program.

In addition to the usual control flow constructs known from all kinds of imperative languages (conditionals, sequences and iterations), Quartz offers synchronous concurrency and preemption statements: The parallel statement \( S_1 \parallel S_2 \) immediately starts the statements \( S_1 \) and \( S_2 \). Then, both \( S_1 \) and \( S_2 \) run in lockstep, i.e. they automatically synchronize when they reach the following pause statement. The parallel statement runs as long as one of the sub-statements is active. The meaning of preemption statements is as follows: A statement \( S \) which is enclosed by an abort block is immediately terminated when the given condition \( \sigma \) holds. Similarly, the suspend statement freezes the control flow in a statement \( S \) when \( \sigma \) holds. Thereby, two kinds of preemption must be distinguished: strong (default) and weak (indicated by keyword weak) preemption. While strong preemption deactivates both the control and data flow of the current step, weak preemption only deactivates the control flow, but retains the current data flow. The immediate variants check for preemption already at starting time, while the default is to check preemption only after starting time.

### B. Compile Problems of Synchronous Languages

On the one hand, perfect synchrony simplifies concurrent programming, since developers do not have to bother about many low-level details like timing, synchronization and scheduling. On the other hand, this paradigm has some consequences that make the compilation of synchronous programs not at all straightforward. In particular, causality and schizophrenia problems challenge compilers.

**Causality problems** [29], [31], [28], [35] arise if statements have cyclic dependencies, i.e. when an action affects its own trigger condition like in the simple program

\[
\text{if}(x) \ x = \text{true}.
\]

Since the condition and the assignment are executed in the same macro step (i.e. in the same variable environment), this statement has no consistent behavior: Assuming \( x = \text{true} \), i.e., \( x = \text{false} \), then \( x = \text{true} \) is not executed, which gives us no reason to assume that \( x \) should be true. Alternatively, assuming \( x = \text{false} \) makes \( x = \text{true} \) true, and hence, \( x = \text{true} \) is executed, which which contradicts the assumption. Hence, this program has no consistent behavior. For this reason, compilers have to perform a causality analysis, which efficiently determines whether a program has a unique behavior that can be constructively determined. In general, even for causally correct programs dynamic schedules are required for their execution.

**Schizophrenia problems** [30], [36] arise from the fact that each variable has a unique value in each macro step. This is a problem for local variables whose scope is left and reentered within a single macro step. Clearly, this can only happen if the local declaration is nested in a loop. As the values of the old and the new scope can be different, local variables must be consequently duplicated in these situations. Hence, when entering a loop body, compilers must create new so-called reincarnations of the variables that are locally declared in a loop body. Since loops can be nested, a locally declared variable may have several reincarnations and the compiler has to keep track of which reincarnation finally propagates its value to the actual local variable.

### III. The Intermediate Format

As already outlined in the introduction, it is quite natural to divide the overall compilation and synthesis process into several

\footnote{We assume here that there is no context statement that could make \( x \) true.}
steps. Due to the use in the design of embedded systems, where hardware-software partitioning and target platforms are design decisions that are frequently changed, persistent intermediate results in a well-defined and robust format are welcome. The synchronous languages community has already defined various intermediate formats \([21], [17], [13], [22], [16], [32]\) including the following ones:

- **Imperative/Intermediate Code (IC):** The IC format is used by imperative languages like Esterel, Argos and Statecharts. It is the compilation result obtained after parsing, expanding macro statements, and type-checking. Hence, no essential code generation is performed, so that the format still offers concurrency, exceptions, and even calls to other modules. The format is therefore close to the kernel Esterel language, and thus, it is still a high-level format.

- **Object Code (OC):** Object code, also called automaton code, is a low-level format that describes a finite state automaton by explicitly listing its states and transitions (and the code that is to be executed along the transitions). Hence, concurrency and complex interaction of threads has already been eliminated by the compiler.

- **(Sorted) Sequential Circuit Code (SC and SSC):** These formats describe a hardware circuit that is obtained by compilation of Esterel programs. The unsorted version SC may contain combinatorial cycles, and the used gates are written in no particular order. In contrast, the SSC format makes use of a topological order and describes an acyclic circuit that may be obtained from a cyclic one by solving the corresponding causality problems.

- **Declarative Code (DC):** The DC format is the privileged interchange format for hardware circuit synthesis, symbolic verification and optimization as well as distributed code generation. The format reflects declarative or data-flow synchronous programs like Lustre, as well as the equational representations of imperative synchronous programs. The underlying idea is the definition of flows by equations governed by clock hierarchies. It is the successor of the previously used GC format \([1]\) and is closely related to the sequential circuit code formats.

- **Event-Triggered Graphs:** Event-based simulation is a characteristic of hardware description languages like VHDL or Verilog. A compilation technique implemented in the Saxo-R T compiler of France Telecom \([11], [10]\) also employed an event-triggered approach, and therefore relays on an internal data structure called an event graph that keeps track of dependencies of events. Hence, an event driven simulation scheme can be used to generate sequential code.

- **Concurrent Control Flow Graphs (CCFG):** The compiler of the University of Columbia translates Esterel programs to concurrent data flow graphs \([15], [16], [22], [13], [14]\). At each instant, the control flow graph is traversed until nodes are reached that correspond with active control flow locations. Then, the corresponding subtrees are executed which changes the values of the control flow locations (that are maintained in Boolean variables). Hence, concurrency is still available, but sequential execution is also supported. Moreover, the format lends itself well for interpretation and software code generation for different architectures.

### A. Guarded Actions

Instead of using one of the existing code formats mentioned above, we chose guarded actions \([9], [12], [18], [19]\), a well-established concept for the description of concurrent systems, as the basis for our intermediate format. This choice is motivated by the following arguments:

- We are convinced that guarded actions are exactly at the right level of abstraction for an intermediate code format, since guarded actions provide a good balance between (1) removal of complex statements available at the source code level and (2) the independence of a specific synthesis target: On the one hand, complex control-flow statements like preemption statements have been eliminated during the translation to guarded actions. Hence, synthesis tools do not have to deal with the complex semantics of difficult (preemption) statements, and instead only have to cope with simple, flat guarded actions. Despite this very simple structure, efficient translation to both software and hardware is possible: OC and SC (which can be both directly derived from guarded actions) bear witness to this. Many intermediate formats do not have this property: whereas IC and CCFG are quite high-level and complex formats for simple synthesis back-ends, OC and SC are target-specific.

- Guarded actions allow many analyses and optimizations. In particular, causality analysis can be performed on guarded actions. If the causality analysis determined that a set of guarded actions does always have a dynamic schedule to compute the output variables without first reading them in each macro step, then even an acyclic set of guarded actions can be determined. Other transformations on guarded actions are the grouping of guarded actions with regard to the variable they modify, which corresponds to the generation of static single-assignment form in the compilation of sequential languages. Note that this transformation does also yield equational code.

- Guarded actions have already been used in the compilation workflow of synchronous programs. For example, they are an explicit intermediate step in the translation to circuit code \([30]\). Hence, our new compilation scheme can benefit from previous results in this area.

Hence, the modules we consider for integration are basically defined by sets of guarded actions of the form \(\gamma \Rightarrow \mathcal{A}\). The Boolean condition \(\gamma\) is called the guard and \(\mathcal{A}\) is called the action of the guarded action, which corresponds to an action of the source language. In this paper, these are the assignments of Quartz, i.e. the guarded actions have either the form \(\gamma \Rightarrow x = \tau\) (for an immediate assignment) or \(\gamma \Rightarrow \text{next}(x) = \tau\) (for a delayed assignment). The semantics of these guarded actions is simply defined as follows: In each macro step, all guards are simultaneously checked. If a guard is true, its action is immediately executed: Immediate assignments instantaneously transfer the computed value to the left-
Guarded actions are generated for the control and data flow of the program. The data flow consists of all assignments of the program and determines the values of all declared variables. The control flow consists of guards $\gamma \Rightarrow \text{next}(\ell) = \text{true}$ where $\gamma$ is a condition that is responsible for moving the control flow at the next point of time to location $\ell$.

This behavior of a system additionally includes implicit guarded actions due to the so-called reaction to absence. It defines the value of a variable if no action has determined its value in the current macro step (obviously, this is the case if the guards of all immediate assignments in the current step and the guards of all delayed assignments in the preceding step of a variable are false). Hence, our intermediate format additionally defines a default reaction for each variable depending on its declared storage type: Event variables are reset to their default values, while memorized variables maintain their previous values.

In principle, the absence reactions can also be expressed as guarded actions. However, this can only be done at the final linking step, since adding additional guarded actions that modify a variable changes the guard of the reaction to absence of this variable. Hence, the reaction to absence can only be determined during the final linking phase. Nevertheless, we can compute its guard in a modular manner, so that the final linker just has to compute the conjunctions of the modularly computed guards. As the guards of these absence reactions have to be modified by the linker, the absence reactions must be kept separately from other actions.

B. Module Instantiations

In addition to the guarded actions of the module itself, the linked module must also include the guarded actions of the called module instances. For a separate compilation, the called modules might not yet be available, so that the compiler can not yet simply include the guarded actions of called modules. For this reason, our intermediate format must provide sections where references to external module calls are collected together with some context information. Due to schizophrenia problems, which have been sketched above, it is possible that more than one reference is created in the intermediate format for a single module call in the source code. Each copy of such a module call will thereby duplicate the actions of the called module.

Fortunately, it is not necessary to duplicate the entire module. Instead, only a copy of its initial behavior (which is the only part of the module that can interfere with other incarnations of the same module) is sufficient. For this reason, we split the guarded actions of a module into a surface and a depth part [6, [34]: the surface of a module consist of those micro steps that are executed at initial time before the first control flow location is reached, while the depth contains the remaining actions of the module.

Due to this distinction, the surface of a module may contain many references to surfaces of called modules, but at most one reference for each module instantiation. In contrast, the depth of a module may contain many surfaces that stem from one module instantiation in addition to its depth. Hence, the surface has a linear growth, while the depth has a quadratic one in terms of the size of the input program.

C. The Context of Modules

Roughly speaking, linking several modules only consists of collecting their guarded actions. However, as module instantiations may occur at arbitrary places in calling modules, the guards of their actions depend on the context of the calling modules: In particular, their guards have to respect activation or preemption conditions given by the context. This information must be passed to the module, and conversely, the module itself must provide status information (e.g. about its activation or termination) to the calling module.

Hence, in addition to the data variables exposed by the interface at the source code level, each AIF module $M$ implicitly provides the following context interface, which is again divided into two parts which affect the surface and the depth of the module. The surface has the following context:

- The $\text{go}^s(M)$ input triggers the execution of the data flow actions in the surface of the module; no program location is affected by this input.
- The $\text{go}^d(M)$ input is responsible for initially activating program locations inside a module. As we maintain the invariant $\text{go}^d(M) \rightarrow \text{go}^s(M)$, the entering of a statement by the control flow implies the execution of its surface. Once the control flow entered program locations inside the statement, these will enable the further execution in the following macro steps.
- The output $\text{inst}(M)$ is true iff the module is currently instantaneous, i.e. if started now, its execution would only consist of micro steps (this depends on the current variables’ values).

$\text{go}^s(M)$ always implies $\text{go}^d(M)$, and $\text{go}^s(M) \land \neg \text{go}^d(M)$ holds iff the statement is weakly pre-empted. The context of the depth has the following interface:

- The input $\text{abrt}(M)$ holds if the context contains an abortion statement whose condition holds. This input does not distinguish between weak and strong abortion (which is relevant only for the data flow), since it is used only for the definition of the control flow of the module.
- The input $\text{susp}(M)$ holds if the context contains a suspension statement whose condition holds. Analogous to $\text{abrt}(M)$, this input does not distinguish between weak and strong suspension (which is also relevant only for the data flow), since it is used only for the definition of the control flow of the module.
- The input $\text{prmt}(M)$ holds if the data flow of the depth of a statement must be preempted due to a surrounding strong abortion or suspension statement. Note that both $\text{abrt}(M)$ and $\text{susp}(M)$ only affect the control flow of a module, and preemption of the surface part is directly handled by the difference of $\text{go}^s(M)$ and $\text{go}^d(M)$. However, a strong preemption of actions in the depth is recognized by the $\text{prmt}(M)$ input only.

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It should be noted that some statements of the source program can belong to the surface and the depth.
The output insd \((M)\) indicates whether a control flow in the module is still active, i.e., whether the module has been started in a previous cycle \((\text{go}^5 (M) = \text{true})\) and its computations have not yet terminated.

The output term \((M)\) is true if \(\text{insd}(M)\) holds and the module currently terminates its computation on its own (i.e., without being aborted).

Recall that \(\text{susp}(M)\) and \(\text{abrt}(M)\) only affect the control flow and therefore, there is no distinction between strong or weak preemption, while \(\text{prmt}(M)\) is used to disable the data flow in the depth and therefore only considers strong preemption without further distinguishing between abortion and suspension. Thus, we have the invariant \(\text{prmt}(M) \rightarrow (\text{susp}(M) \lor \text{abrt}(M)).\)

## D. AIF at a Glance

To summarize, the intermediate format has the following structure\(^7\):

- **name** (module name)
- **interface** (list of variables exposed at the data interface)
- **locals** (list of locally declared variables)
- **surface**
  - **context** (surface context)
  - **surfaceCalls**
  - **ctrlFlow** (guarded actions of the control flow)
  - **dataFlow** (guarded actions of the data flow)
- **depth**
  - **context** (depth context)
  - **surfaceCalls**
  - **depthCalls**
  - **ctrlFlow** (guarded actions of the control flow)
  - **dataFlow** (guarded actions of the data flow)
- **absReacts** (absence reactions)

The Averest system defines a scheme for a Extensible Markup Language (XML) file, which is used as the concrete syntax.

## IV. SEPARATE COMPILATION

The translation of synchronous programs to guarded commands is already a well-established procedure. The previous compiler of the Averest system used it as an intermediate step for the generation of equations or transition systems. We refer to [30] for a detailed presentation of the previous translation.

### A. Adding Modularity

However, [30] (as all other documented compilers) does not support modular compilation: Some procedures of previous compilers made use of a global view of the system and considered information about the entire system. For example, for each local variable declaration, it is counted how many loops are nested around it, so that the number of required reincarnations could be easily determined. In a modular compilation, this information is no longer available, and therefore the compilation procedure must be fundamentally changed:

The deactivation of the data flow, which is necessary when a strong preemption takes place, cannot be implemented by reprocessing the previously computed guarded actions. Previously, this was done each time a strong preemption context was compiled: the guard of each action was strengthened by the preemption guard (which requires that all guarded actions including the ones from other modules, are known). Instead, the new compiler uses the context signal input \(\text{prmt}(M)\), which is implicitly added to all guards of the module. As already outlined above, this signal only affects the depth of the module. Strong preemption in the surface is handled by the input \(\text{go}^5(M)\).

Although the guarded actions were computed in a recursive traversal over the syntax tree of the source program, the resolution of schizophrenia problems was deferred to the very end. All incarnations of a module were counted and afterwards, new incarnations are created depending on this index. However, in the modular case, the number of incarnations for any other module is not accessible. Hence, a multi-dimensional numbering of the incarnations is needed: from the conceptual point, not single variables are duplicated, but instead, the surface of a module (including other surfaces) is duplicated with the potentially included copies of local declarations. To this end, our new compiler makes use of qualified names (QNames), which are used to distinguish variable names that stem from the same name but are used in different module calls and reincarnations.

A very tricky problem related to schizophrenia is posed by delayed assignments which are executed in the last step in the scope of the local variable. These updates must be always deactivated, since the scope of the variable is left. If the scope would be instantaneously reentered, the delayed assignment of the previous scope would interfere with the current scope, which would be wrong. This problem has already been solved for the non-modular case, but the modular case introduces a new technical challenge, since output variables of a module can be associated to local variables of the calling module: A delayed assignment to an output variable of a module \(M\) must be deactivated if this variable is associated with a local variable by a module call to \(M\) if at this point of time the scope of the local variable is left. Hence, this deactivation cannot be made when compiling the called module. However, also the calling module does not yet know the guarded actions of the inner module. Hence, the deactivation must be deferred to the linker, but the compiler has to endow all argument expressions of the module calls by appropriate (de)activation conditions (see previous section), which are finally added to the guards of the linked module.

### B. Compiler Structure

The main idea is to compute in one pass over a given statement \(S\) the required information like the context information and the guarded actions of the surface and the depth. To this end, the translation has to forward start conditions \(\text{go}^5\) and \(\text{go}^6\) during

\(^7\)The AIF format additionally contains specifications, which is neglected in this paper for the sake of simplicity.
the recursive descent. Additionally, the translation maintains
the conditions ab, sp and pr, thus keeping track of surrounding
abortion and suspension conditions. The essential key to solve
schizophrenia problems is to compile the surface and depth
parts of statements separately, so that the occurrences of the
local variables in the surface parts can be renamed according
to their incarnation levels. The complete pseudo code for the
compiler is given in the appendix. Its core are the two sets of
functions XSurface (Figure 2) and XDepth (Figure 3), which
compile the surface and depth part of a given statement type X.
Both are initially triggered by the function StartCompile,
which also subsequently combines the result of both functions
for the final AIF file.

The function Surface computes the surface part with help
of the following parameters:
- \( L \) is the list of local declarations
- \( \rho \) is the substitution for renaming local names
- \( go^e \) is the condition for executing the data actions of the
  surface
- \( go^d \) is the condition for entering the depth of the statement
- \( S \) the statement to be compiled

The result is a tuple \((I, D^s, T, M^e, C^d)\), where
- \( I \) is the condition that holds iff the statement is instantaneous
  (i.e. \( inst(S) \)),
- \( D^s \) is the list of guarded actions of the surface,
- \( T \) is the list of actions of the surface to transfer values
determined by renamed local variables \( \xi \) to the corresponding
variable \( x \) in the depth \( (T) \) is a list of pairs
\((x, (go^e, \xi))\),
- \( M^e \) is the list of module calls made in the surface (each
  one consists of an instance name, the module’s name, the
  argument expressions with a possible disabling condition,
as well as the surface context conditions \( go^e, go^d, I \) to
connect the module call later by the linker, and
- \( C^d \) is the list of guarded actions for the control flow
  (delayed actions \( \phi \Rightarrow next(\ell) = true \) for a label \( \ell \)).

There are no real surprises in the code for the surface, so that
there is not much to explain. Only the translation of delayed
actions on local variables needs more explanation: If a surface
is activated and weakly preempted (i.e. \( go^e \land \neg go^d \) holds),
then \( go^d \) is usually used as guard of actions of the data flow except
for delayed actions on local variables: These delayed actions
must not be enabled, since their local scope is left after the
current step. Hence, \( go^d \) is used instead of \( go^e \) for delayed
actions on local variables.

The function Depth computes the depth of a statement. It
takes the following parameters:
- \( h \): the depth of the next surface (an integer to rename
  local names)
- \( sp \): condition to suspend the control flow
- \( ab \): condition to abort the control flow
- \( pr \): condition to preempt the data flow (i.e. the disjunction
  of the surrounding strong abort/suspend conditions)
- \( S \) the statement to be compiled

The result of this function is a tuple \((L, N, A, T, D^s, A, M^e, M^d, C^d)\) with:
- \( L \) is the list of locally declared variables of \( S \)
- \( N \) is the list of the module instance names
- \( A \) is equivalent to \( insd(S) \)
- \( T \) is equivalent to \( term(S) \)
- \( D^s \) is the list of guarded actions for the depth of \( S \)
- \( A \) is the list of absence reactions for the locally declared
  variables (a list of triples \((lhs, cL, dw)\) where \( lhs \) is a
  lhs of a locally declared variable, \( cL \) is list of the guarded
  cases and \( dw \) the default value), renamed surfaces to the
  corresponding local variable in the depth,
- \( M^e \) is the list of module calls to surfaces of other
  modules contained in the depth (see sequences, loops,
  and immediate suspension),
- \( M^d \) is the list of module calls to depths of other modules
  that are called in the depth,
- \( C^d \) is the list of transition equations for the depth of \( S \)

The compilation of the depth of a statement is done by a
function call \( Depth(h, sp, ab, pr, S) \). In addition to the state-
ment \( S \), the function call has the parameters \( sp, ab \) and \( pr \),
which indicate whether a surrounding suspension or abortion
statement is preemting the execution. These conditions are
refined at this point a new context of a preemption statement is
traversed. The compilation of the depth of a statement leads
to subsequent calls to the depth compilation of its sub-
statements, and it may additionally lead to the compilation of
some surfaces of the sub-statements. This is the case
for sequences, loops and the immediate weak suspension
statement. The compilation of the depth must then not only
provide an adequate renaming \( \rho \), but must also provide the start
signals \( go^d \) and \( go^e \) for the surface compilation.

V. EXAMPLE

Consider the following two modules:

```
module M1(bool ?i, bool !o){
  if(i) { \ell : pause; o = i; } else o = true;
}
```

- surface (context: \( inst(M1) = \neg i \))
  - control flow: \((go^e(M1) \land i, next(\ell) = true)\)
  - data flow: \((go^d(M1) \land \neg i, o = true)\)
- depth (context: \( insd(M1) = \ell \); term(M1) = \ell) 
  - data flow: \((\ell \land \neg prmt(M1), o = i)\)

```
module M2(bool ?i, bool !o){
  loop {
    bool b;
    weak abort M1(i,b); when(b);
    \ell : pause;
    o = b;
  }
}
```

- surface (context: \( inst(M2) = false \)
  - control flow: \((go^d(M2), next(\ell) = true)\)
- depth \{ \( insd(M2) = \neg insd(M1) \) \land \ell \land term(M2) = false \}
  - control flow:
    \((\neg abrt(M2) \land \ell \land \neg term(M1) \land next(\ell) = true)\)
  - data flow: \((\ell \land \neg prmt(M2), o = b)\)

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• absence
  \[- \begin{cases} b, & \text{if } \text{go}^2(M_2) \text{ then } = b^4 \text{ else false} \end{cases}\]
In addition, module $M_2$ stores three references to $M_1$ in its intermediate format: one depth and two surfaces, one with parameter $b$ and the other one with its reincarnation as parameter, $b_1$.

VI. CONCLUSIONS
In this paper, we show how modular/separate compilation for imperative synchronous languages can be achieved. Our compilation scheme is based on an intermediate code format that holds the information computed by the compiler that is later required by the linker. The reincarnation problem is handled by reincarnating the module calls with a sophisticated qualification of variable names so that the linker has not to care about local variables (hence, local variables are for the first time really treated locally in the compilation!). We developed a translation procedure for the whole language Quartz, including programs containing schizophrenia problems.

REFERENCES
function CompileModule(module Module(args) {S})
g0 := NewParam();
g0 := NewParam();
sp := NewParam();
ab := NewParam();
pr := NewParam();
(I, D0, T, M1, C0) := Surface({}, {g0, g0, S});
(L, N, A, T, D1, A, M2, M3, C0) := Depth(0, sp, ab, pr, S);
module args L, I, D0, M1, C0,
return {A, T, D0, M2, M3, C0, C0, A};

function CombineABS(A, T)
A' := {A};
forall (x, T, dx) ∈ A
T' := T ∪ (∪ (x, w, v, s) ∈ T(x, v, w, s));
A' := A' ∪ T';
return (A);

function SurfaceRenaming(h, L, N)
ρ1 := ∪ (∈ x → x);
ρ2 := ∪ (∈ x → x);
return (ρ1 ∪ ρ2);

function DisableDelayedAssignment(ψ, x, G)
(G1 := (γ ∧ ψ ⇒ next(x) = τ) (γ ⇒ A) ∊ G ∧
(G2 := (γ ⇒ next(x) = τ) (γ ⇒ A) ∊ G ∧
return (G1 ∪ G2);

function DisableDelayedArgument(ψ, x, M)
M' := {ψ};
forall (C, inst, Module, (p0, a0), (p1, a1), (p2, a2)) ∈ M
for i ∈ {0, . . . , n}
if p0 = x then a0 := a0 ∧ ψ else a0 := a0;
M' := M' ∪ (C, inst, Module, (p0, a0), (p1, a1), (p2, a2));
return (M');

function ConditionalSurface(L, ρ, g0, g0, σ, S1, S2)
g0 := NewDef(g0 ∧ ρ(σ));
g0 := NewDef(g0 ∧ ρ(σ));
g0 := NewDef(g0 ∧ ρ(σ));
(L, D0, T, M1, C1) := Surface(L, ρ, g0, g0, S1);
(L, D0, T, M2, C2) := Surface(L, ρ, g0, g0, S2);
I := NewDef(I ∧ L);
I := NewDef(I ∧ L);
I := NewDef(I ∧ L);
return (I, D0 ∪ T, M1 ∪ M2, C1 ∪ C2);

function ParallelSurface(L, g0, g0, S, S1, S2)
(I, D0, T, M1, C1) := Surface(L, ρ, g0, g0, S1);
(I, D0, T, M2, C2) := Surface(L, ρ, g0, g0, S2);
I := NewDef(I ∧ L);
return (I, D0 ∪ T, M1 ∪ M2, C1 ∪ C2);

function SequenceSurface(L, g0, g0, S, S1, S2)
return ParallelSurface(L, ρ, g0, g0, S1, S2);
do S while(σ);
return Surface(L, ρ, g0, g0, S);
abort S when(σ):
return Surface(L, ρ, g0, g0, S);
weak abort S when(σ):
return Surface(L, ρ, g0, g0, S);
abort S when immediate(σ):
return AbortImmSurface(L, ρ, g0, g0, σ, S, false);
weak abort S when immediate(σ):
return AbortImmSurface(L, ρ, g0, g0, σ, true);
suspend S when(σ):
return Surface(L, ρ, g0, g0, S);
weak suspend S when(σ):
return Surface(L, ρ, g0, g0, S);
weak suspend S when immediate(σ):
return SuspendImmSurface(L, ρ, g0, g0, S, false);
weak suspend S when immediate(σ):
return SuspendImmSurface(L, ρ, g0, g0, σ, true);
weak suspend S when immediate(σ):
{a : S}:
return SuspendImmSurface(L, ρ, g0, g0, σ, true, σ, true);
if storage(a) is memorized
then T2 = ({x : g0, pre(x)}) else T2 = {};
M' := DisableDelayedArgument(M1, x, ¬g0);
return (I, D0, T1 ∪ T2, M', C');
inst := name(argL);
I := NewParam();
M' := {g0 ∧ ρ(σ) ⇒ next(l) = true};
if wek then
return (I, D0, T, M', C');

Fig. 1. Compilation Algorithm (1/3): Start and Auxiliary Functions

Fig. 2. Compilation Algorithm (2/3): Surface Computation
Fig. 3. Compilation Algorithm (3/3): Depth Computation