

Control-flow Guided Property Directed Reachability for Imperative Synchronous Programs

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MEMOCODE 2016 at IIT Kanpur, November 18 – 20, 2016

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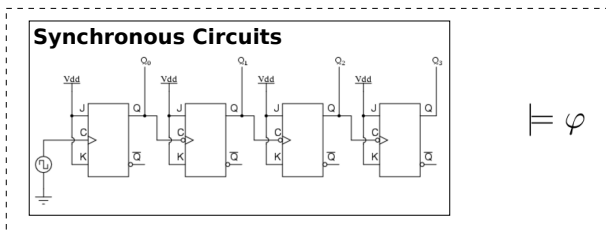
1. Motivation
2. Property Directed Reachability
3. Control-flow Guided PDR for Imperative Synchronous Programs

Outline

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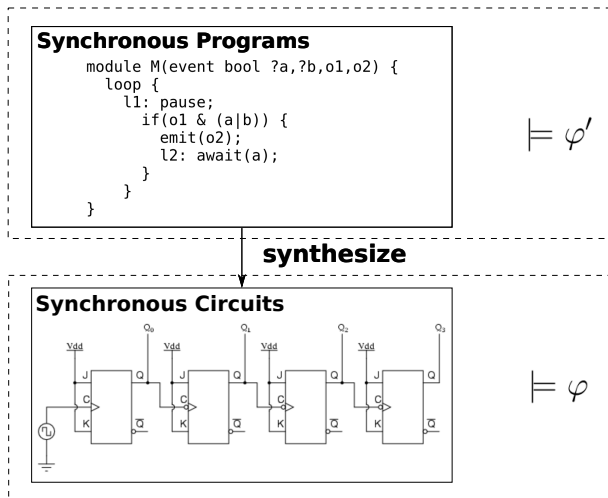
Formal Verification of Synchronous Hardware Circuits

- ▶ PDR: a very efficient verification method based on induction



Formal Verification of Synchronous Programs

- ▶ PDR: a very efficient verification method based on induction



Imperative Synchronous Programs

Imperative Synchronous Languages: e.g. Quartz

- ▶ macro steps: consumption of one logical time unit
 - ▶ micro steps: no logical time consumption
- ⇒ synchronous reactive model of computation

Control-flow Information

- ▶ not needed for synthesis
- ▶ useful for formal verification

Goals

Target: Safety Property Verification of Imperative Synchronous Programs

- ▶ PDR: relies on good estimation of the reachable states

Our Heuristic: Improve it by Reachable Control-flow States Computation

- ▶ linear-time static analysis
- ▶ symbolic reachability analysis

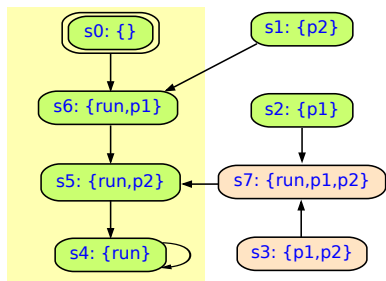
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Safety Property Verification

Target: Prove Φ is valid w.r.t. \mathcal{K}

- ▶ a state transition system: $\mathcal{K} := (\mathcal{V}, \mathcal{I}, \mathcal{T})$
- ▶ a safety property: Φ
- ▶ Φ holds on all reachable states of \mathcal{K}



Φ holds



Φ doesn't hold



Reachable States

```

module CfSeq() {
    p1: pause;
    p2: pause;
}
  
```

$\mathcal{V} := \{\text{run}, p1, p2\}$

$\mathcal{I} := \neg(\text{run} \vee p1 \vee p2)$

$\mathcal{T} := \text{next}(\text{run}) \leftrightarrow \text{true}$
 $\wedge (\text{next}(p1) \leftrightarrow \neg\text{run})$
 $\wedge (\text{next}(p2) \leftrightarrow p1)$

$\Phi := \neg(p1 \wedge p2)$

Safety Property Verification by Induction

Target: Prove Φ is valid w.r.t. \mathcal{K}

- ▶ a state transition system: $\mathcal{K} := (\mathcal{V}, \mathcal{I}, \mathcal{T})$
- ▶ a safety property: Φ
- ▶ Φ holds on all reachable states of \mathcal{K}

Φ is inductive w.r.t. \mathcal{K}

- ▶ induction base: Φ holds in all initial states
- ▶ induction step: Φ -states have no successor violating Φ

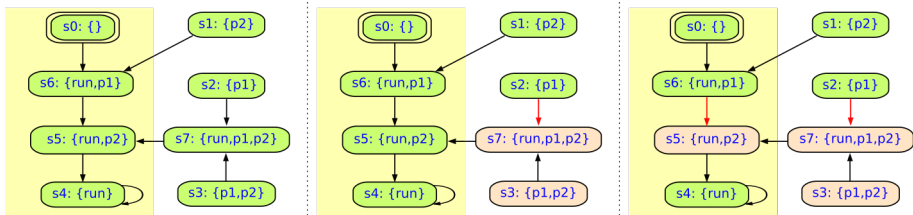
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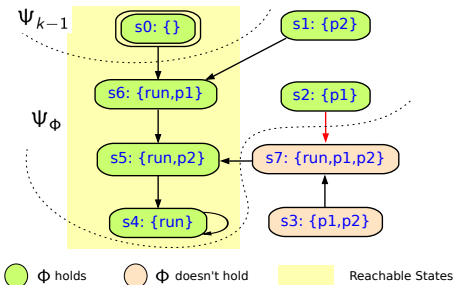
- ▶ induction base: Φ holds in all initial states
- ▶ induction step: Φ -states have no successor violating Φ



Property Directed Reachability

PDR method constructs a sequence of clause sets Ψ_0, \dots, Ψ_k that overapproximate the states reachable in $0, \dots, k$ steps.

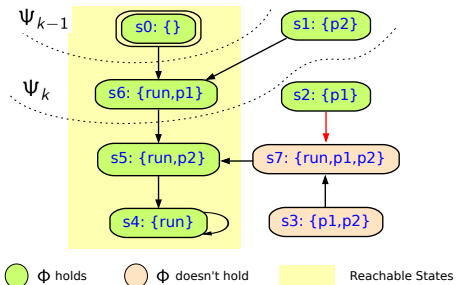
- ▶ incremental induction: extend the sequence Ψ_0, \dots, Ψ_k
- ▶ unreachability checking:
identify counterexamples to induction (CTIs)



Property Directed Reachability

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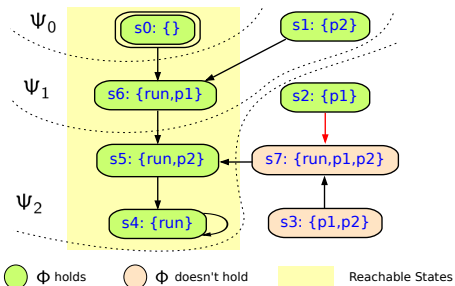




Property Directed Reachability

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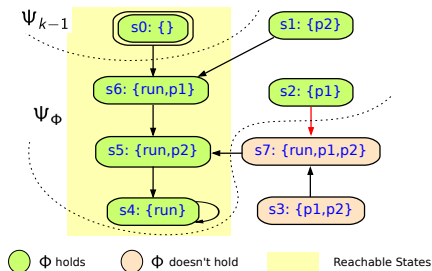


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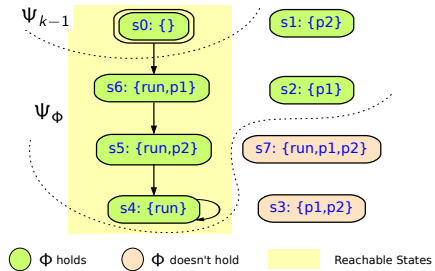
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Main Idea: Modify Transition Relation to generate less CTIs

Original Transition Relation:

 s_2 has successor s_7 violating Φ

Enhanced Transition Relation:

 s_2 has no successor \Rightarrow remove transitions from unreachable states by **control-flow invariants**

Control-flow Invariants by static Analysis

Control-flow can never be active at both substatements of sequences and conditional statements:

```
module CfSeq(){  
    p1: pause;  
    p2: pause;  
}
```

$\neg(p1 \wedge p2)$

Control-flow Invariants by static Analysis

Control-flow can never be active at both substatements of sequences and conditional statements:

```
module Ite(){  
  mem bool i;  
  if (i) {  
    p1: pause;  
  } else {  
    q1: pause;  
  }  
}
```

$\neg(p1 \wedge q1)$

Control-flow Invariants by static Analysis

Control-flow can never be active at both substatements of sequences and conditional statements:

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module CfIte(){
  mem bool i;
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    p2: pause;
  } else {
    q1: pause;
    q2: pause;
  }
}
```

$$\neg(p1 \wedge p2) \wedge \neg(q1 \wedge q2) \wedge \neg((p1 \vee p2) \wedge (q1 \vee q2))$$

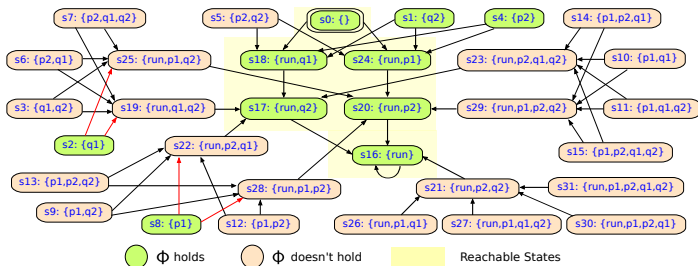
Control-flow Invariants by static Analysis

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    q2: pause;
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}

```

Original Transition Relation:



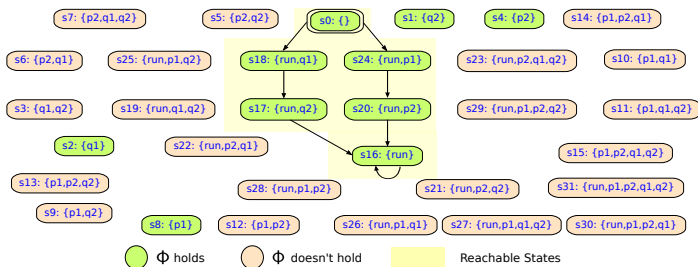
Control-flow Invariants by static Analysis

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  }
}

```

Enhanced Transition Relation:



with control-flow invariant by static analysis:

$$\neg(p_1 \wedge p_2) \wedge \neg(q_1 \wedge q_2) \wedge \neg((p_1 \vee p_2) \wedge (q_1 \vee q_2))$$

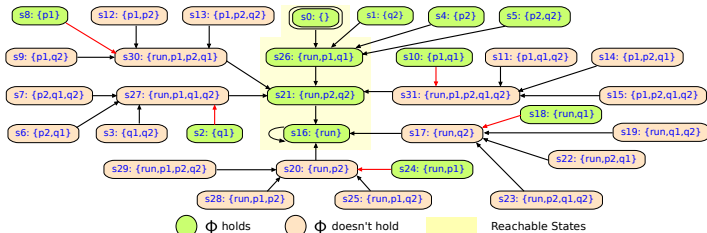
Control-flow Invariants by symbolic Analysis

```

module CfPar(){
  {
    p1: pause;
    p2: pause;
  } ||
  {
    q1: pause;
    q2: pause;
  }
}

```

Original Transition Relation:



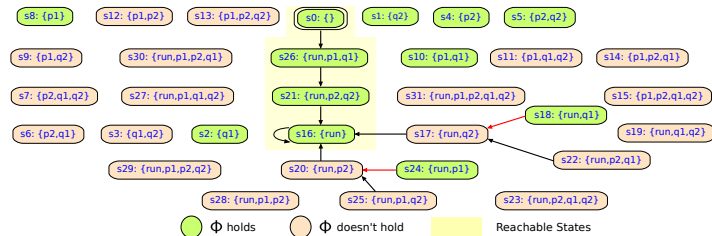
Control-flow Invariants by symbolic Analysis

```

module CfPar(){
  {
    p1: pause;
    p2: pause;
  } ||
  {
    q1: pause;
    q2: pause;
  }
}

```

Enhanced Transition Relation:



with control-flow invariant by static analysis:

$$\neg(p1 \wedge p2) \wedge \neg(q1 \wedge q2)$$

Control-flow Invariants by symbolic Analysis

Symbolic traversal of the state space of the control-flow system:

```
module CfPar(){
  {
    p1: pause;
    p2: pause;
  } ||
  {
    q1: pause;
    q2: pause;
  }
}
```

$$\neg(p1 \wedge p2) \wedge \neg(q1 \wedge q2) \wedge \neg((p1 \vee p2) \wedge (q1 \vee q2))$$

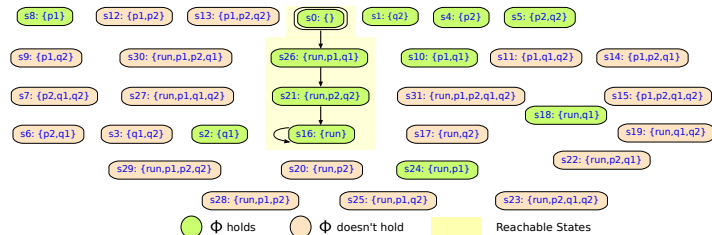
Control-flow Invariants by symbolic Analysis

```

module CfPar(){
  {
    p1: pause;
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  } ||
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    q1: pause;
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}

```

Enhanced Transition Relation:



with control-flow invariant by symbolic analysis:

$$\neg(p_1 \wedge p_2) \wedge \neg(q_1 \wedge q_2) \wedge \neg((p_1 \vee p_2) \wedge (q_1 \vee q_2))$$

Summary

Control-flow Guided PDR for Imperative Synchronous Programs

- ▶ two methods for reachable control-flow states computation
 - ▶ linear-time static analysis
 - ▶ symbolic reachability analysis
- ⇒ different precision and runtime complexities
- ▶ enhanced transition relation makes PDR more efficient
 - ⇒ save arbitrarily many incrementation steps of PDR